

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): KAJIHARA, et al.  
Serial No.: 09/989,242  
Filed: November 21, 2001  
For: LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE USING THE SAME, AND METHOD OF AND  
PROCESS FOR FABRICATING THE TWO  
Group: 2815  
Examiner: Jasmine Jhihan B Clark  
Conf. No.: 9603

**RESPONSE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

November 29, 2011

Sir:

In response to the Office Action dated September 29, 2011, the remarks are  
set forth on the following pages:

**Remarks**